



Attorney Docket No.: P-6381-US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): WILK, Brian et al. Examiner: LEE, Wilson D
Serial No.: 10/748,170 Group Art Unit: 3723
Filed: December 31, 2003 Assignee: Intel Corporation
Title: SUPPORT SYSTEM FOR SEMICONDUCTOR WAFERS AND METHODS THEREOF

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

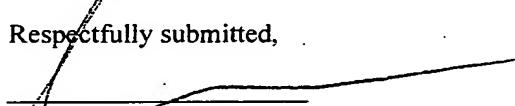
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Sir:

Applicants would like to thank the Examiner for the allowance of the subject application based on the distinctions enumerated in the "Statement for Reasons of Allowance". Applicants agree, without prejudice, that these distinctions provide sufficient basis for allowance of the application; however, Applicants respectfully submit that there may be alternative and/or additional patentable distinctions between the claimed invention and the cited prior art, related or unrelated to those explicitly addressed in the "Statement of Reasons for Allowance".

Should the Examiner have any question or comment as to the form, content or entry of this paper, the Examiner is requested to contact the undersigned counsel at the telephone number below.

Respectfully submitted,


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Dated: April 10, 2006

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